

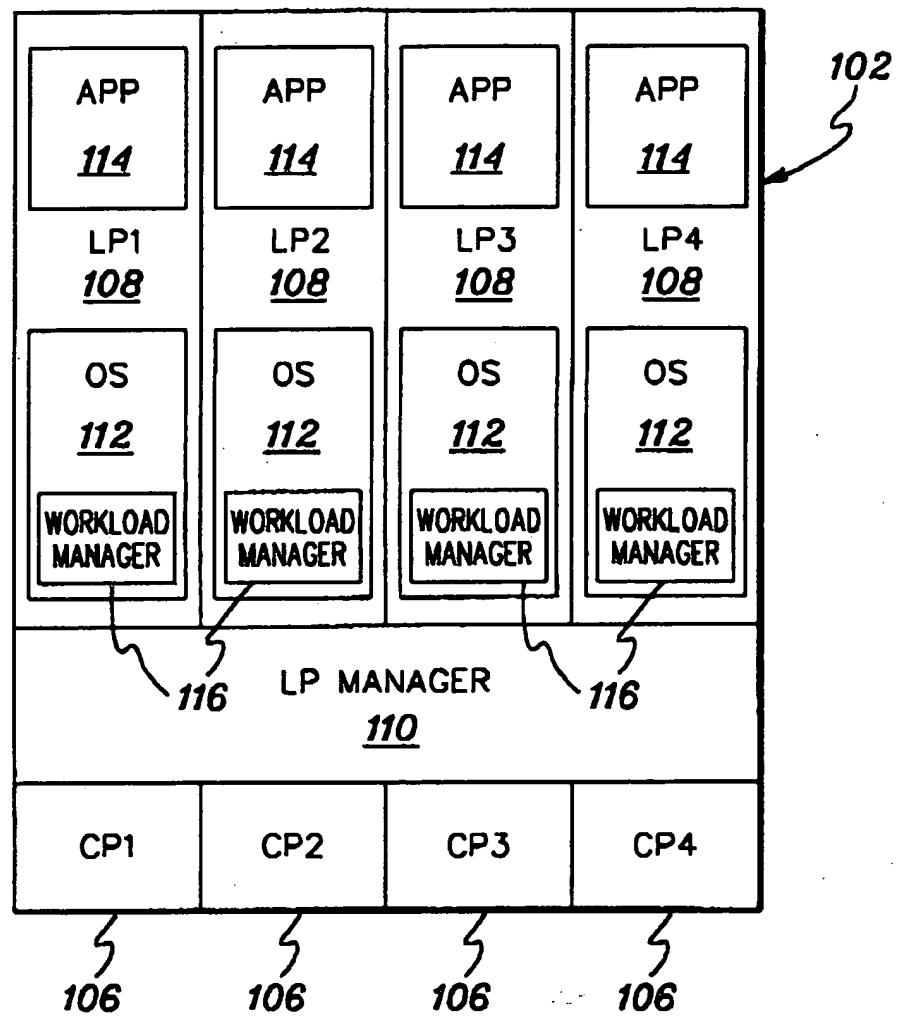
100

fig. 1A

2/22  
Peg - 99 - 158

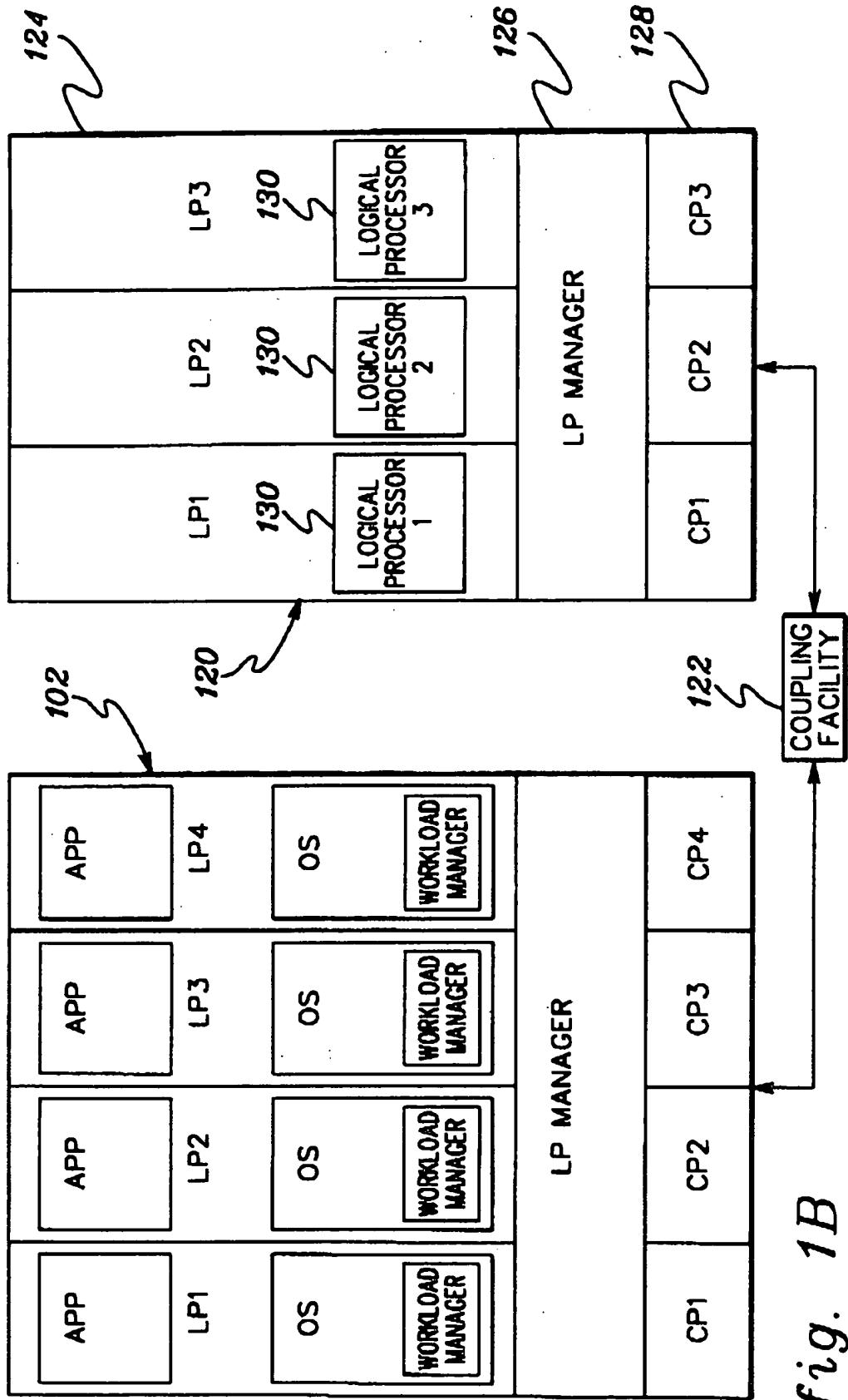


fig. 1B

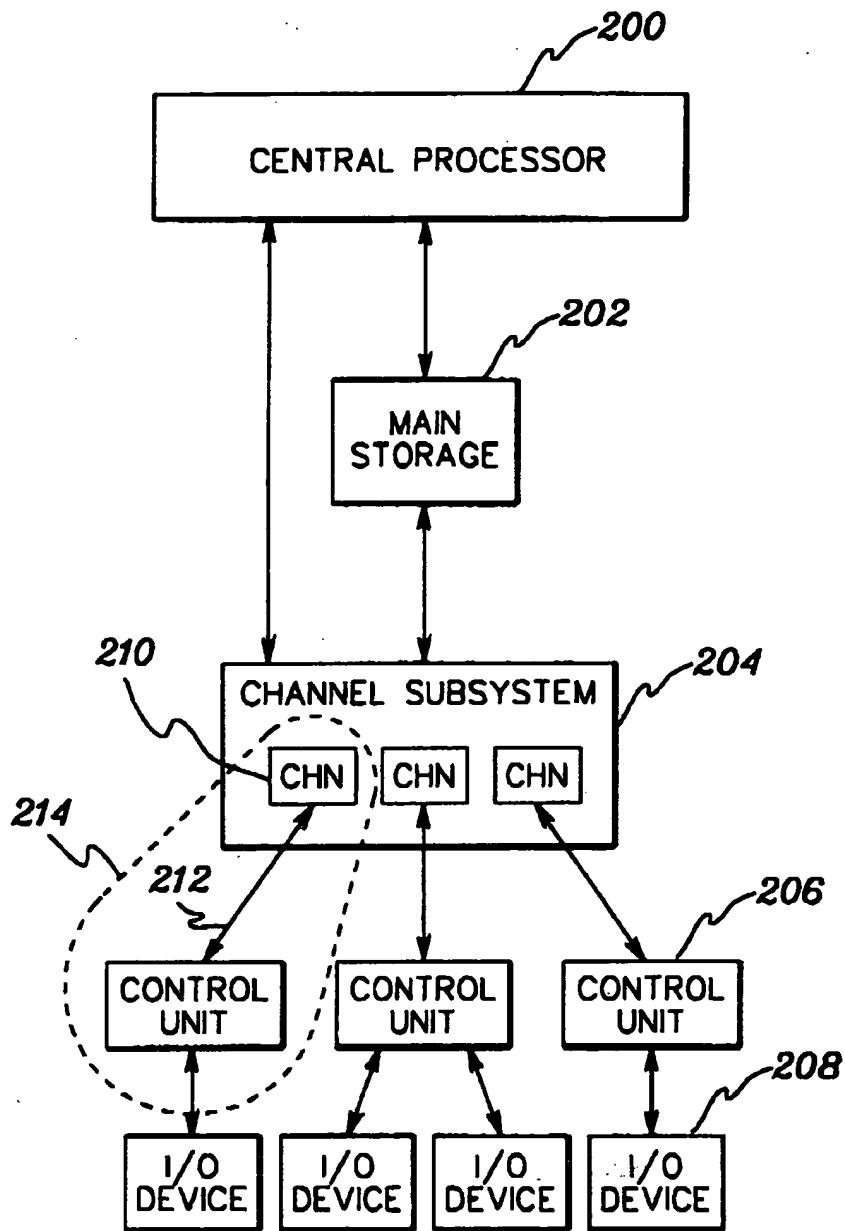


fig. 2

4/22

PCP-99-158

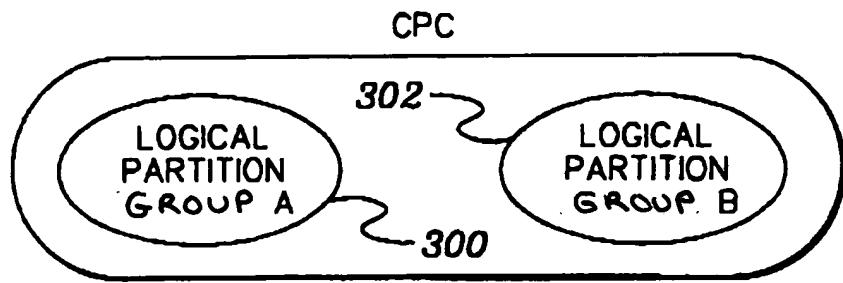


fig. 3

JOIN A GROUP

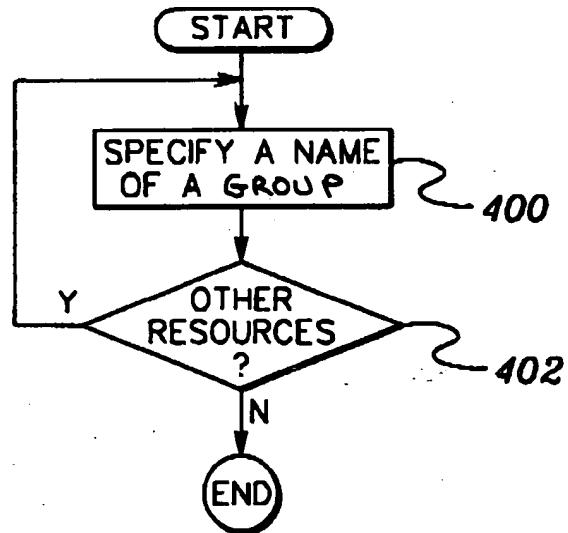
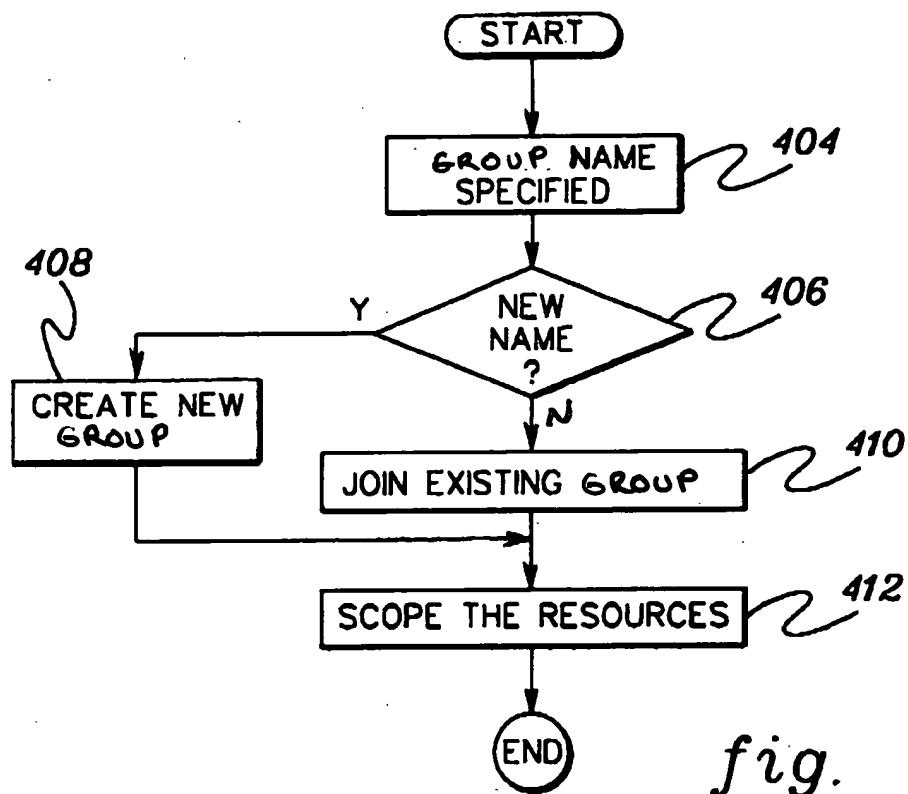
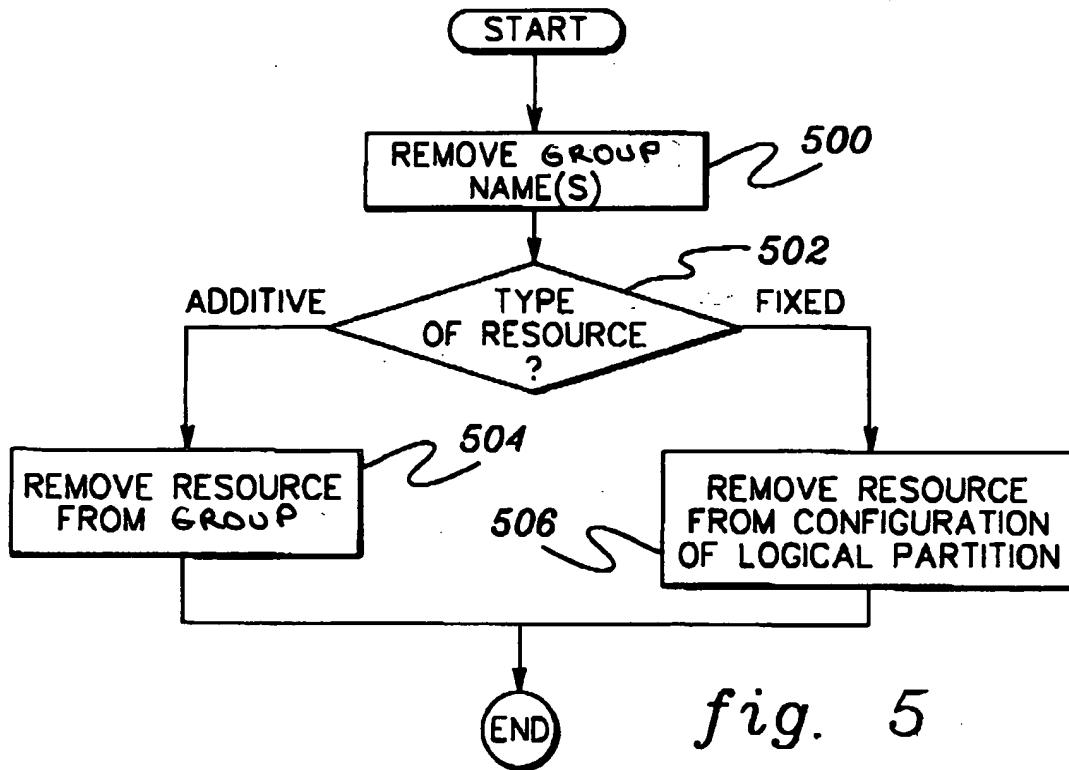


fig. 4A



REMOVE FROM A GROUP



6/22  
P09-99-158

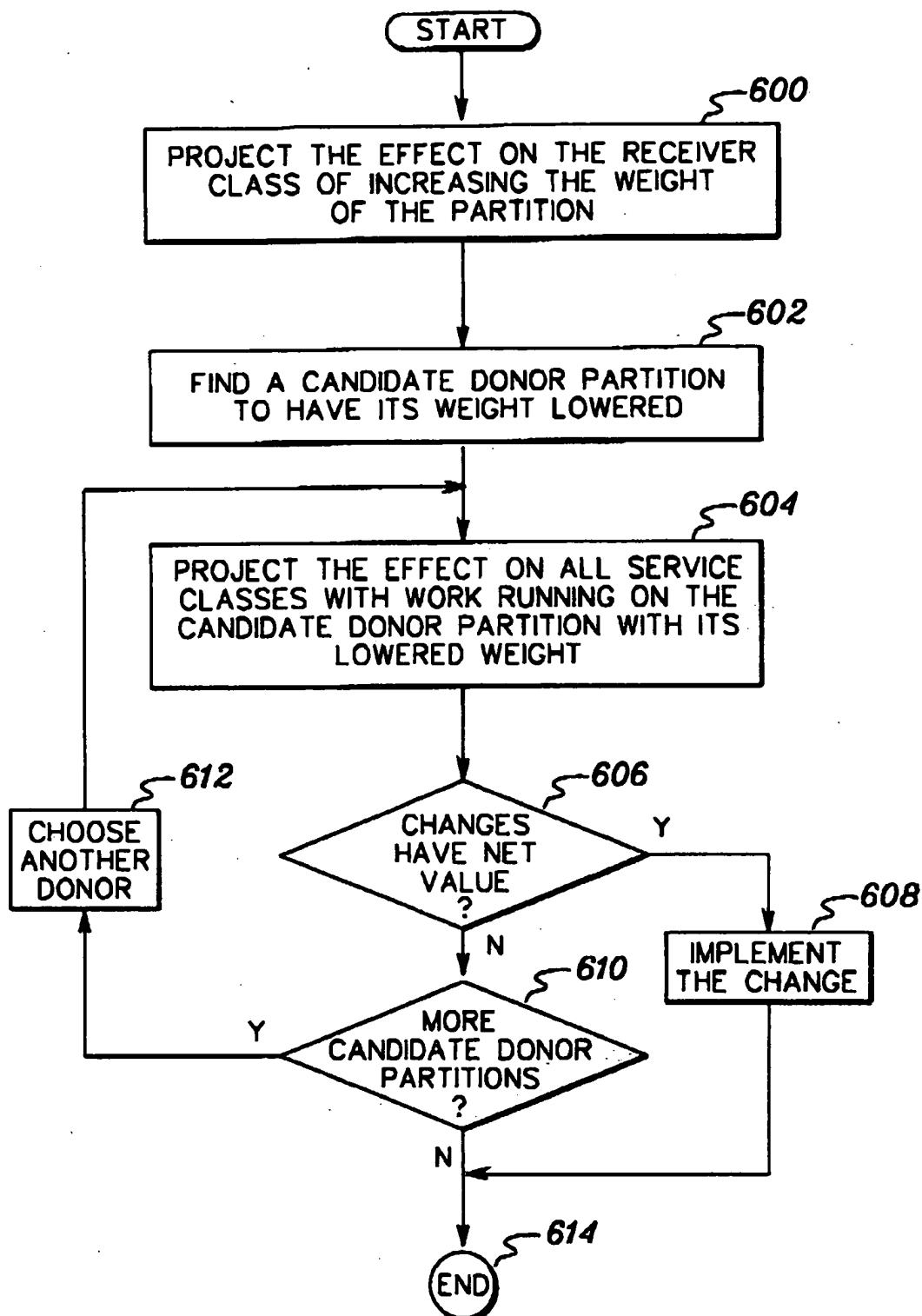


fig. 6

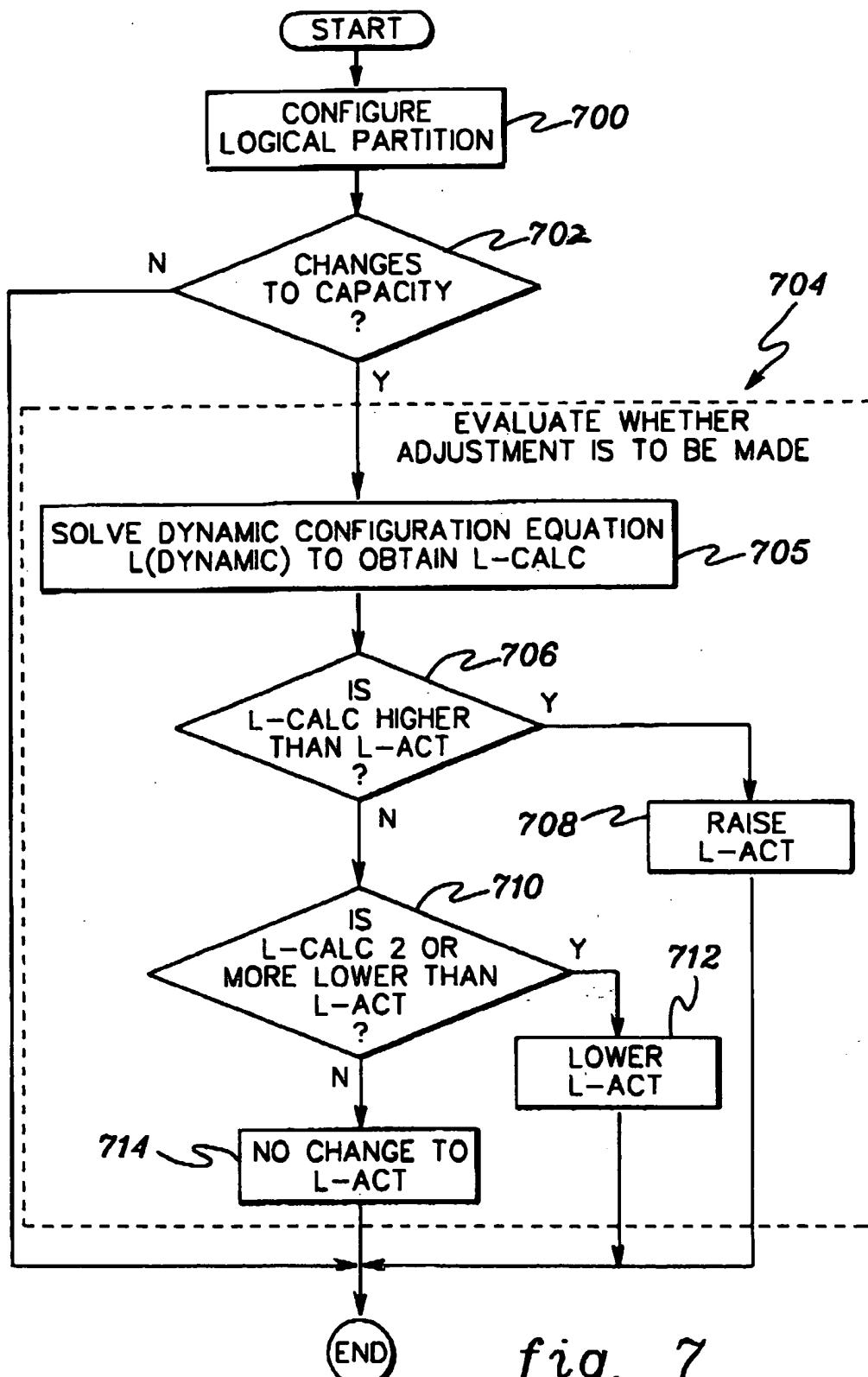
DYNAMIC ADJUSTMENT OF CONFIGURATION  
OF LOGICAL PROCESSORS

fig. 7

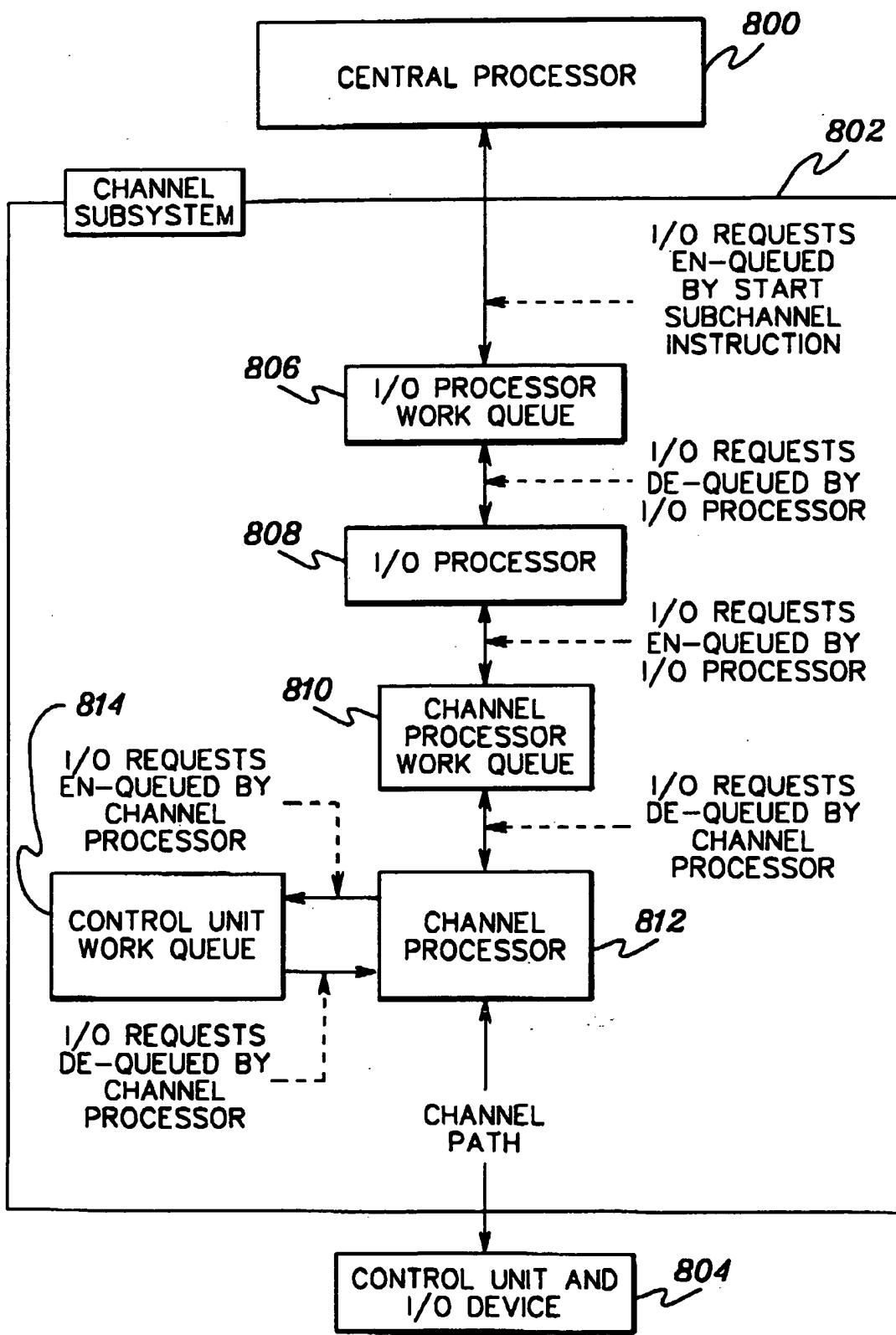


fig. 8

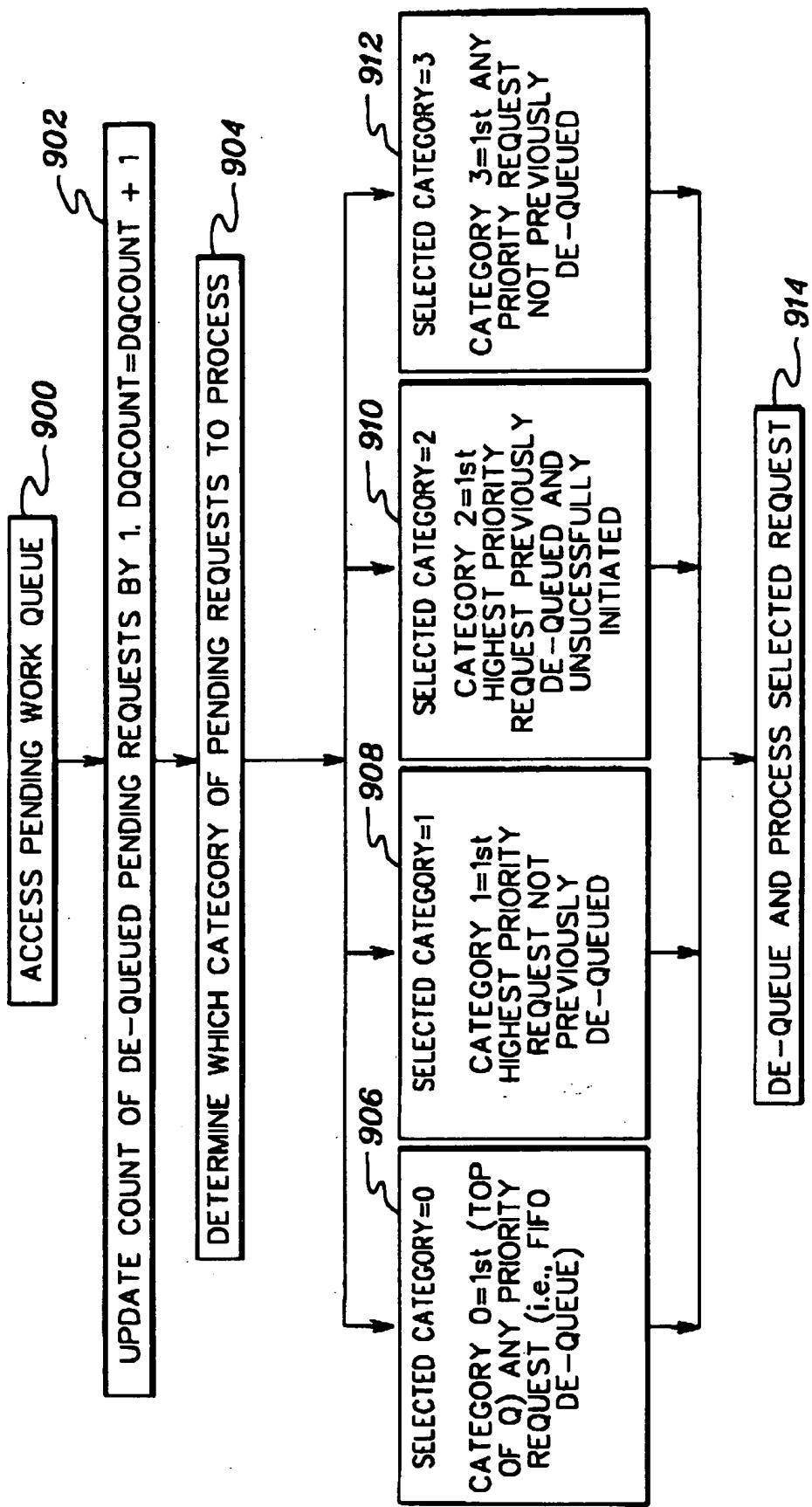


fig. 9

10/22  
P09-99-158

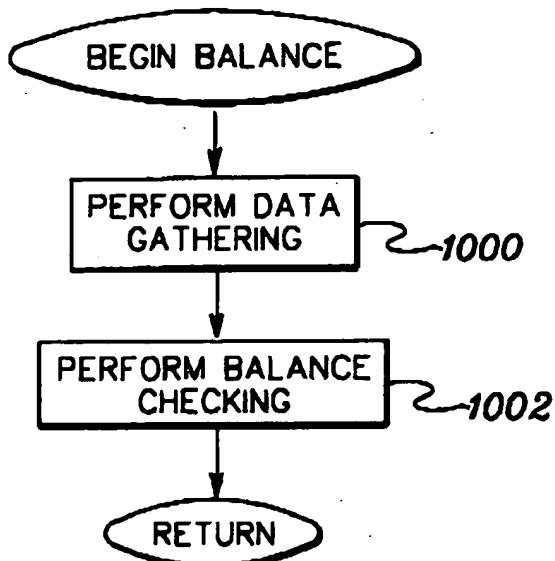


fig. 10

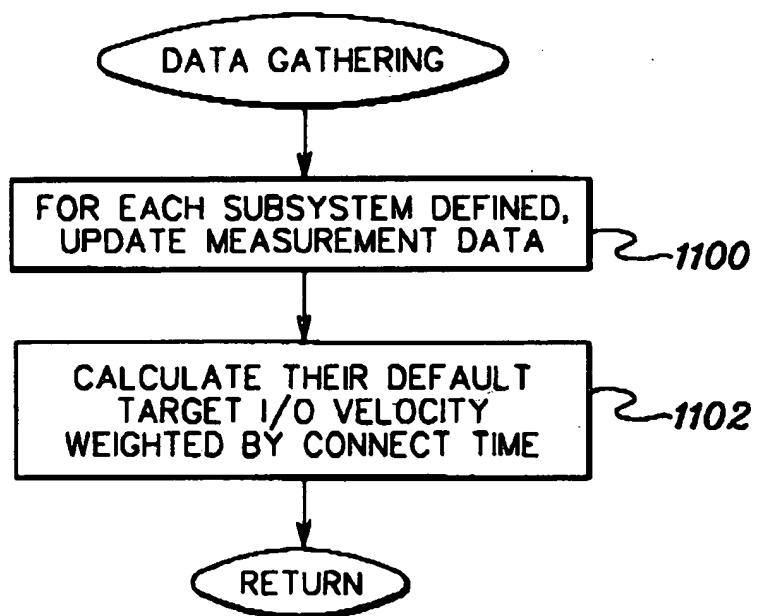


fig. 11

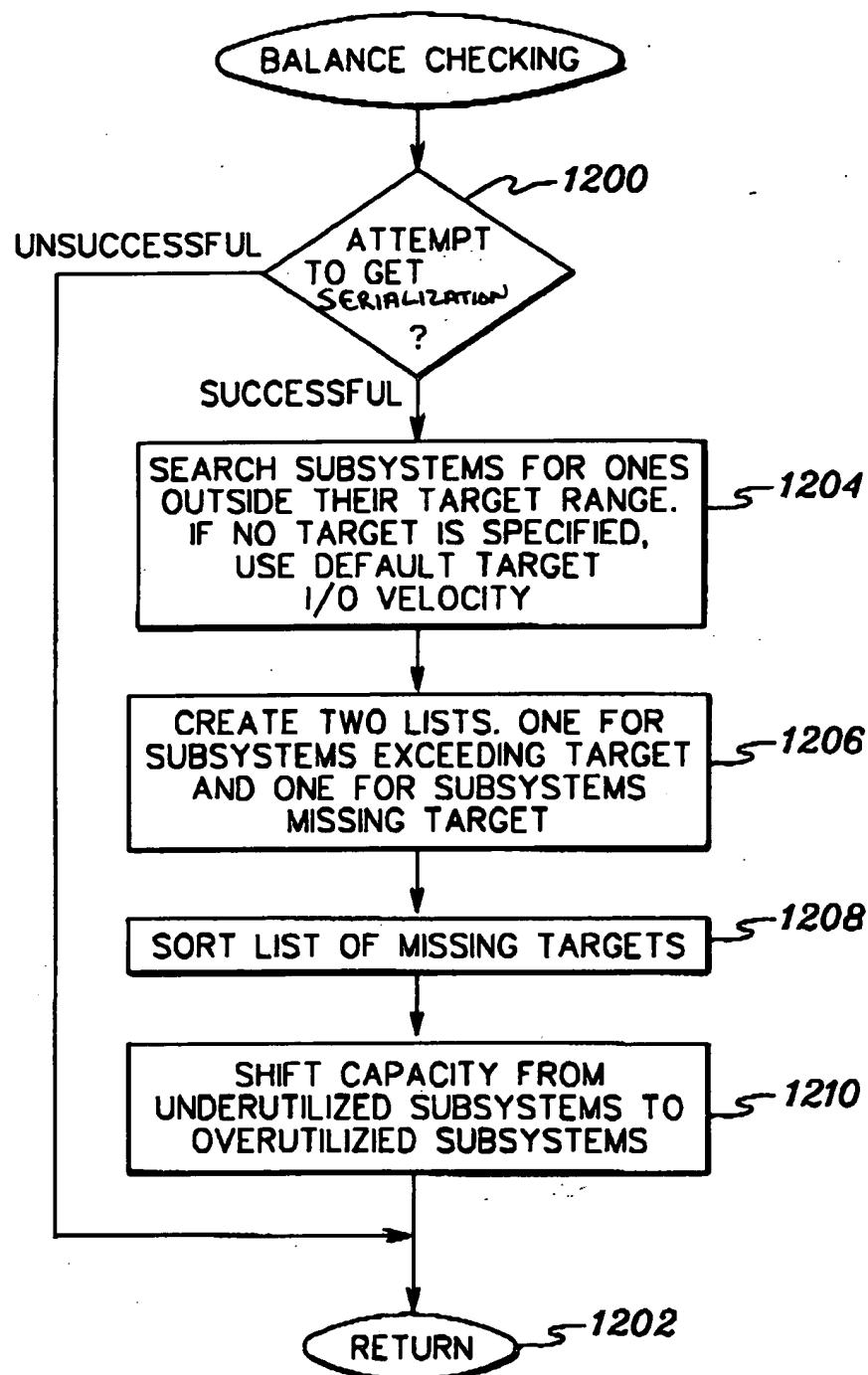
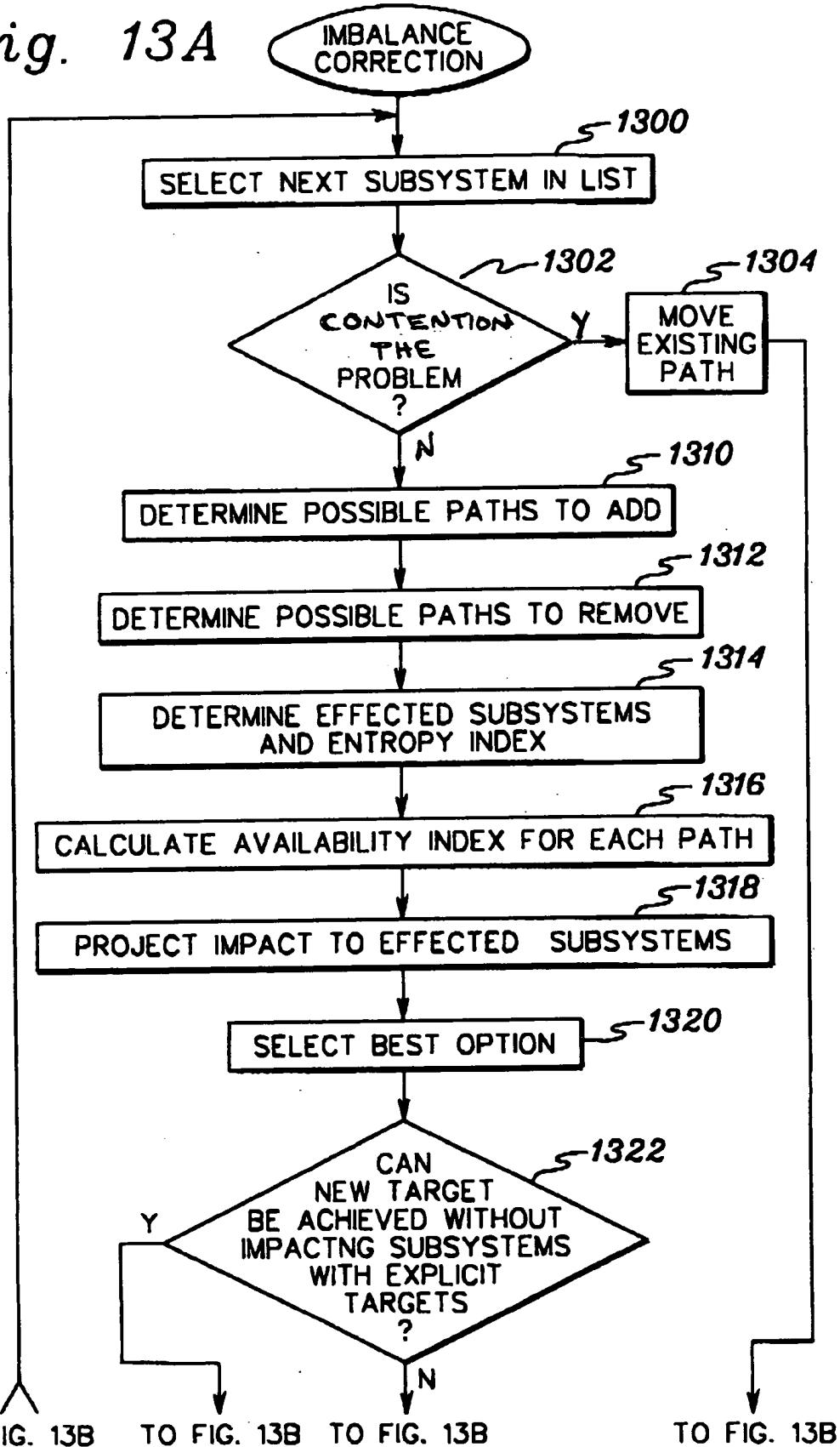
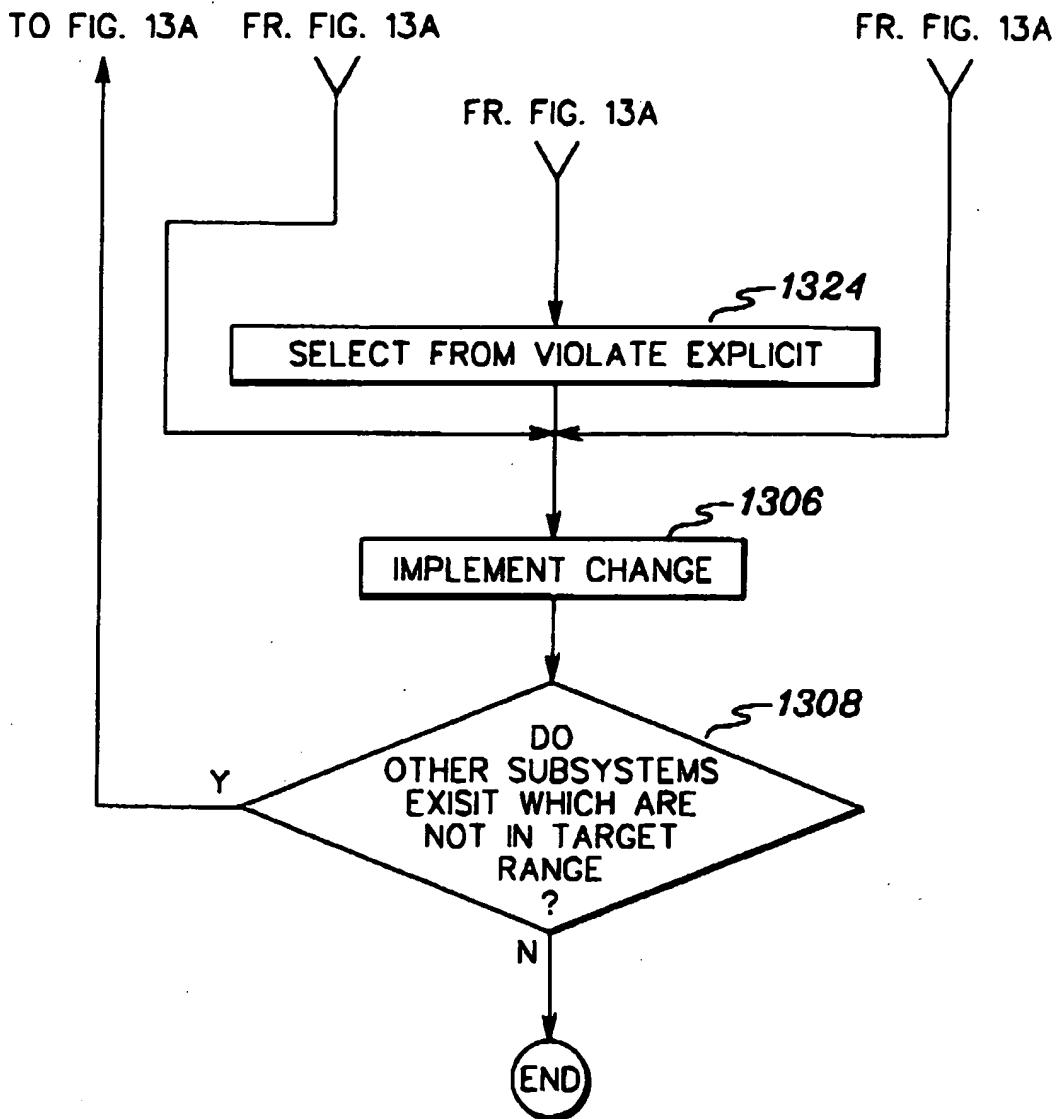


fig. 12

fig. 13A





*fig. 13B*

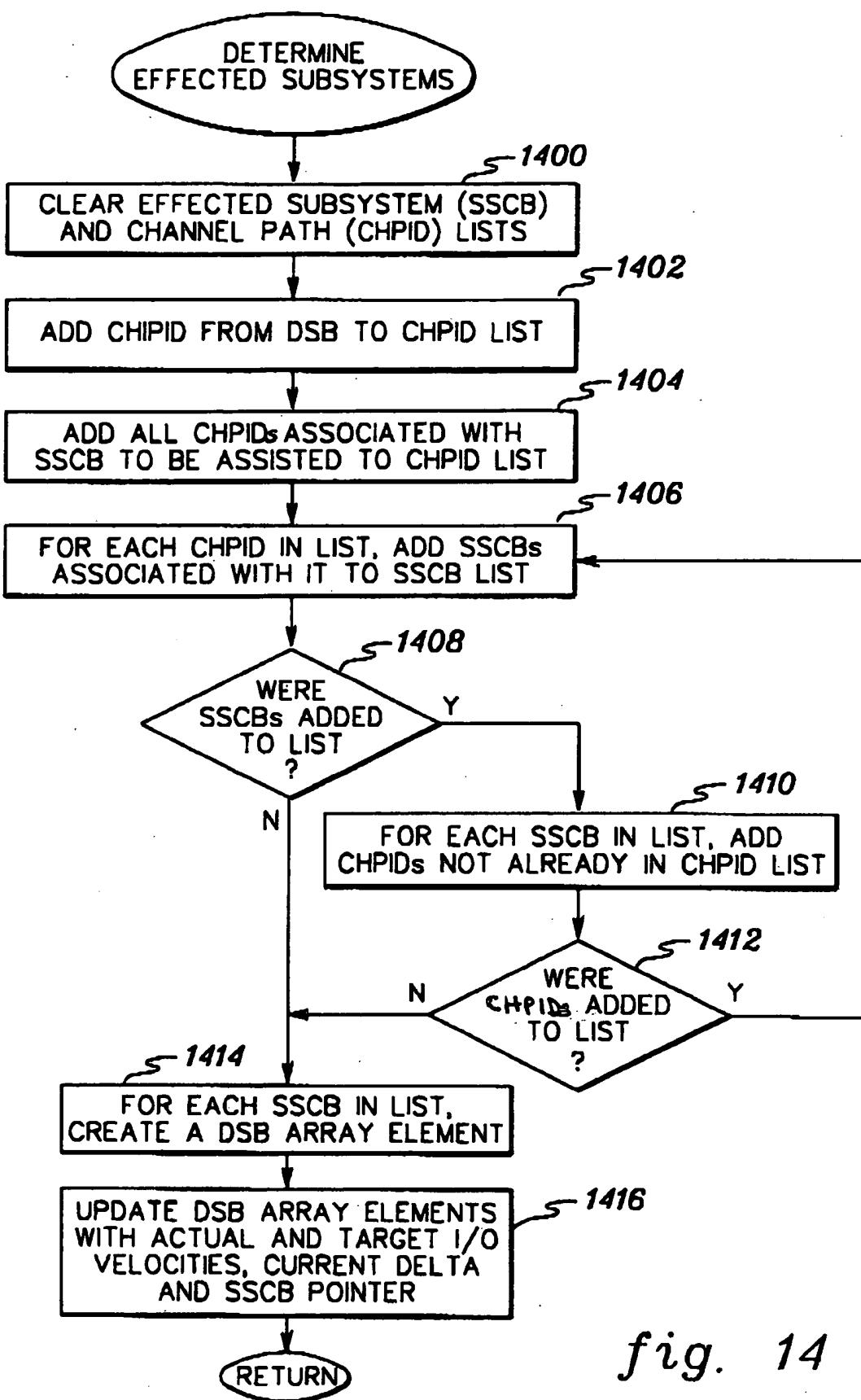


fig. 14

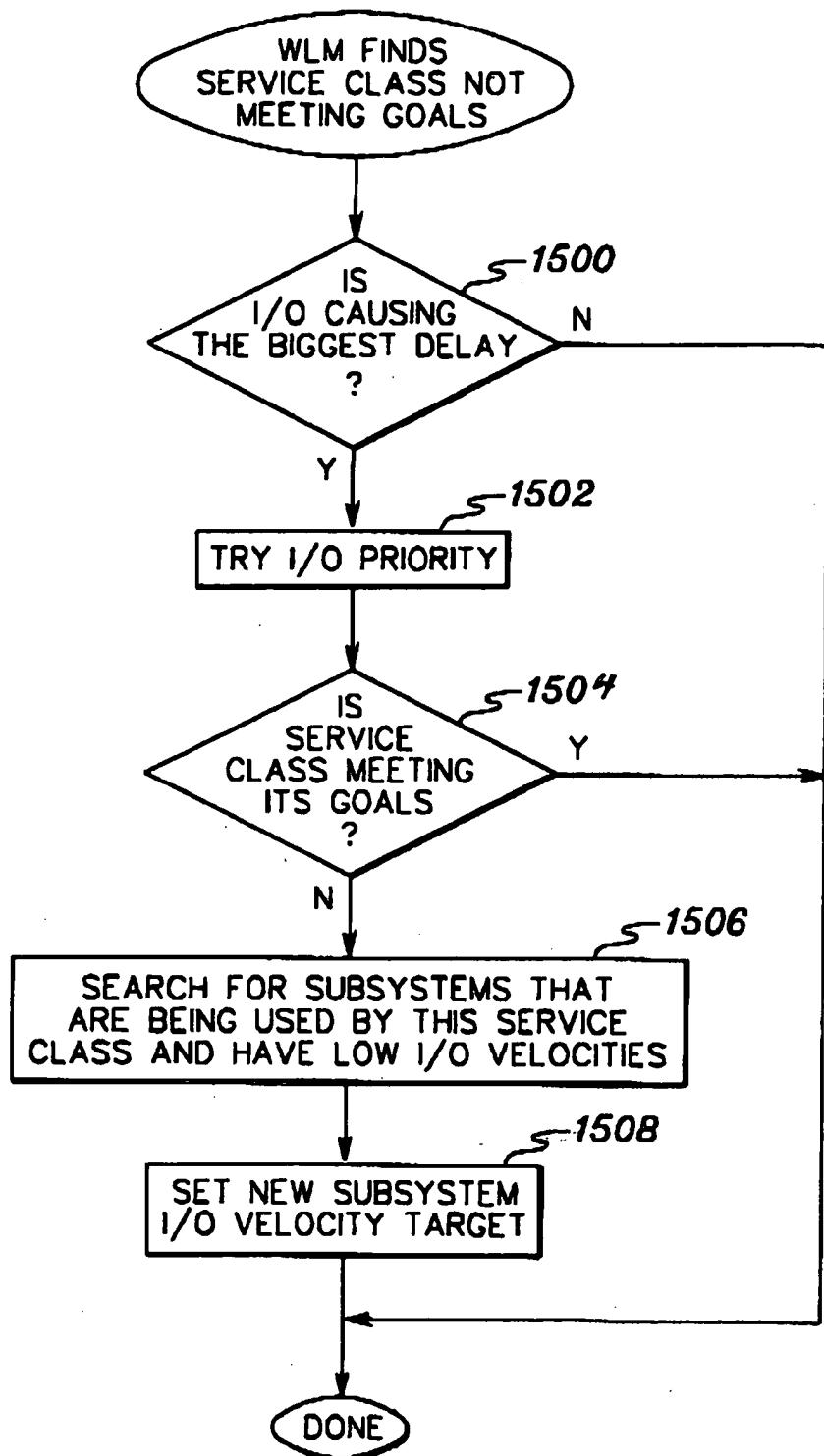


fig. 15

16/22  
P09-99-158

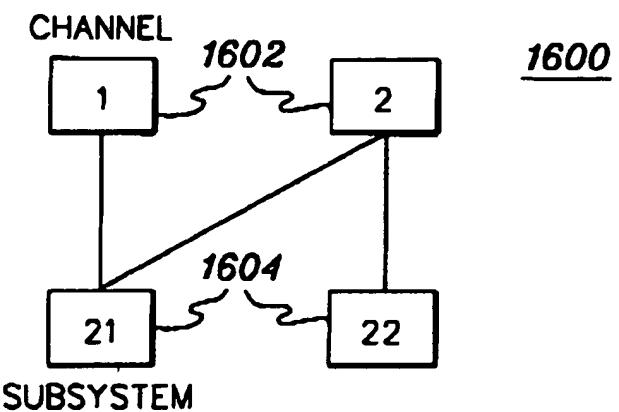


fig. 16A

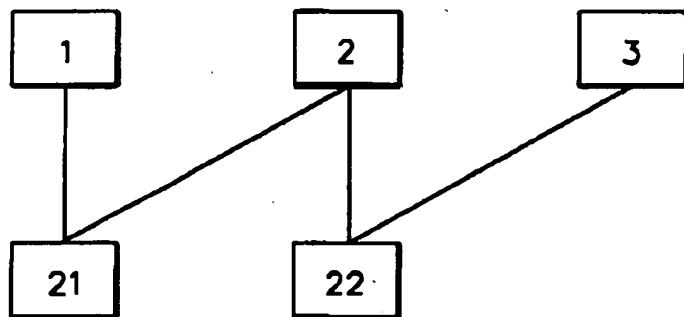


fig. 16B

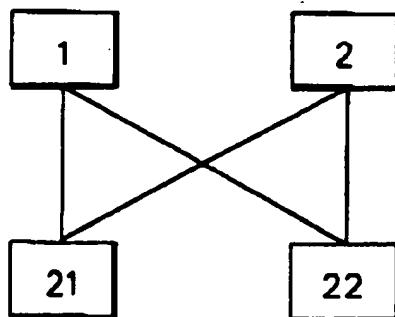


fig. 16C

17/22  
PO9-99-158

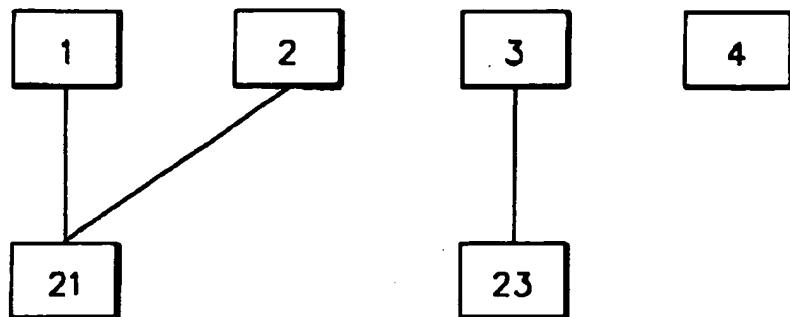


fig. 17A

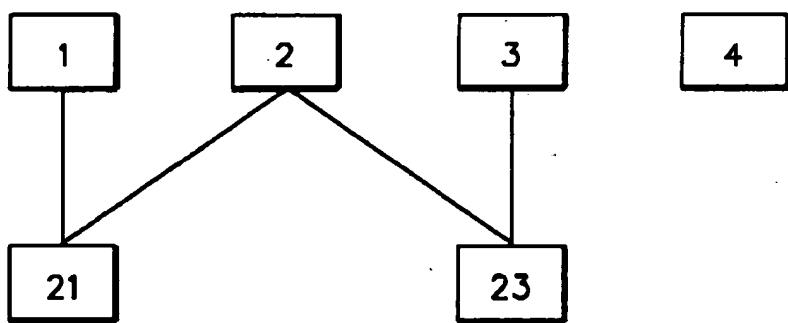


fig. 17B

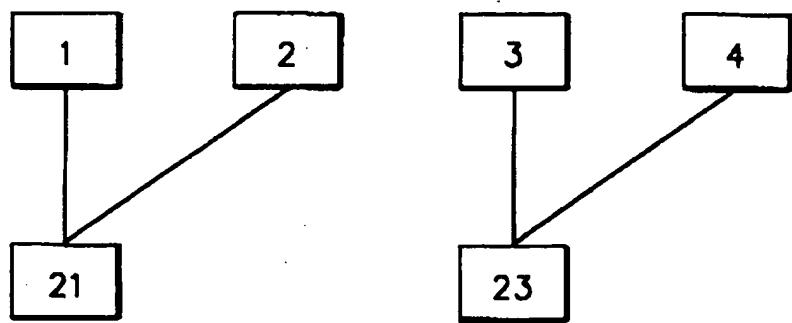


fig. 17C

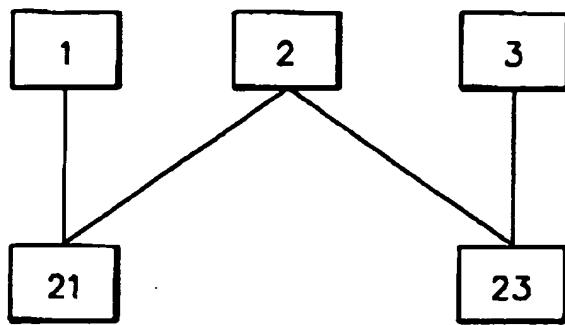


fig. 18A

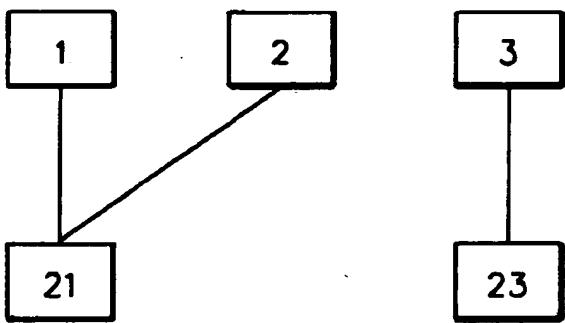


fig. 18B

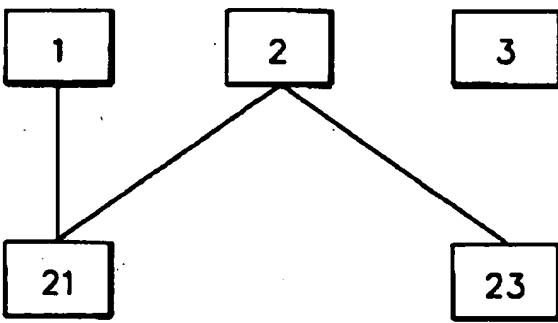


fig. 18C

19/22  
869-99-158

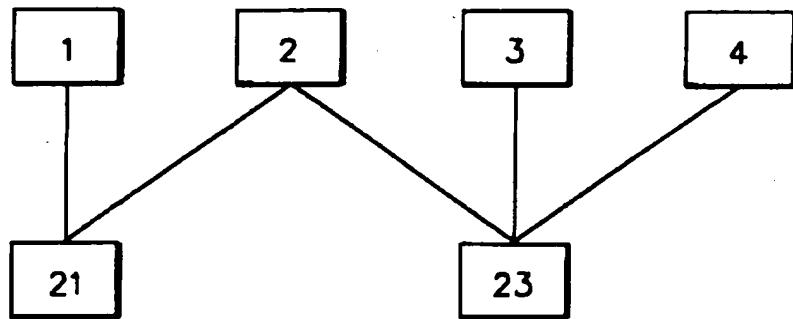


fig. 19A

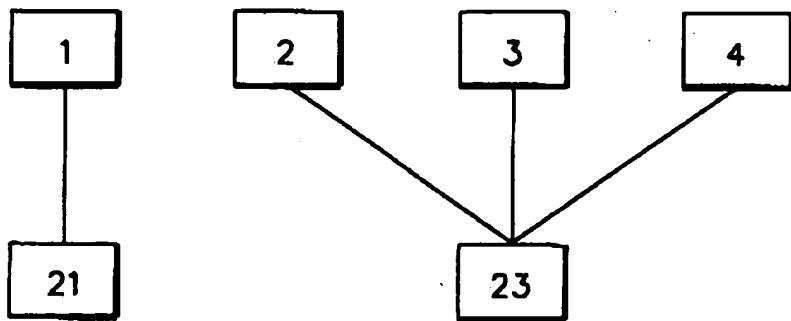


fig. 19B

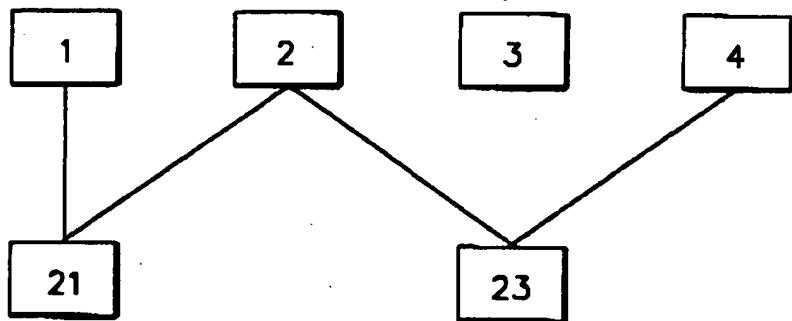


fig. 19C

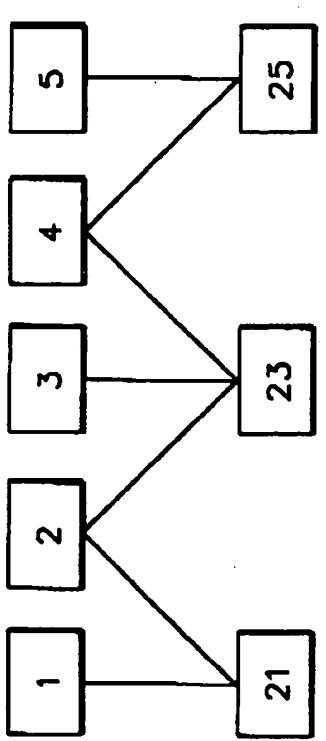


fig. 20A

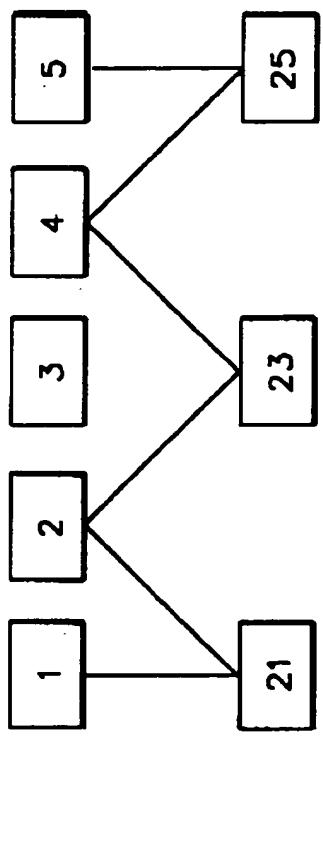


fig. 20B

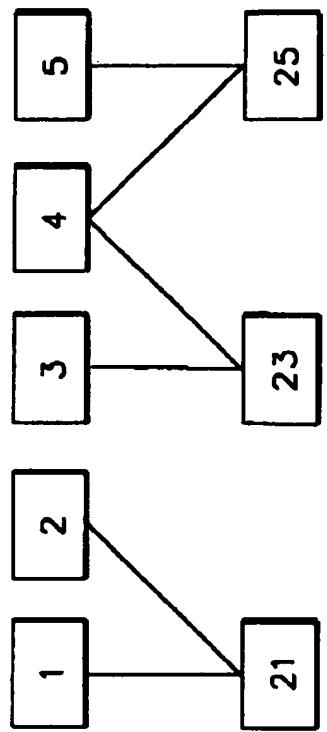


fig. 20C

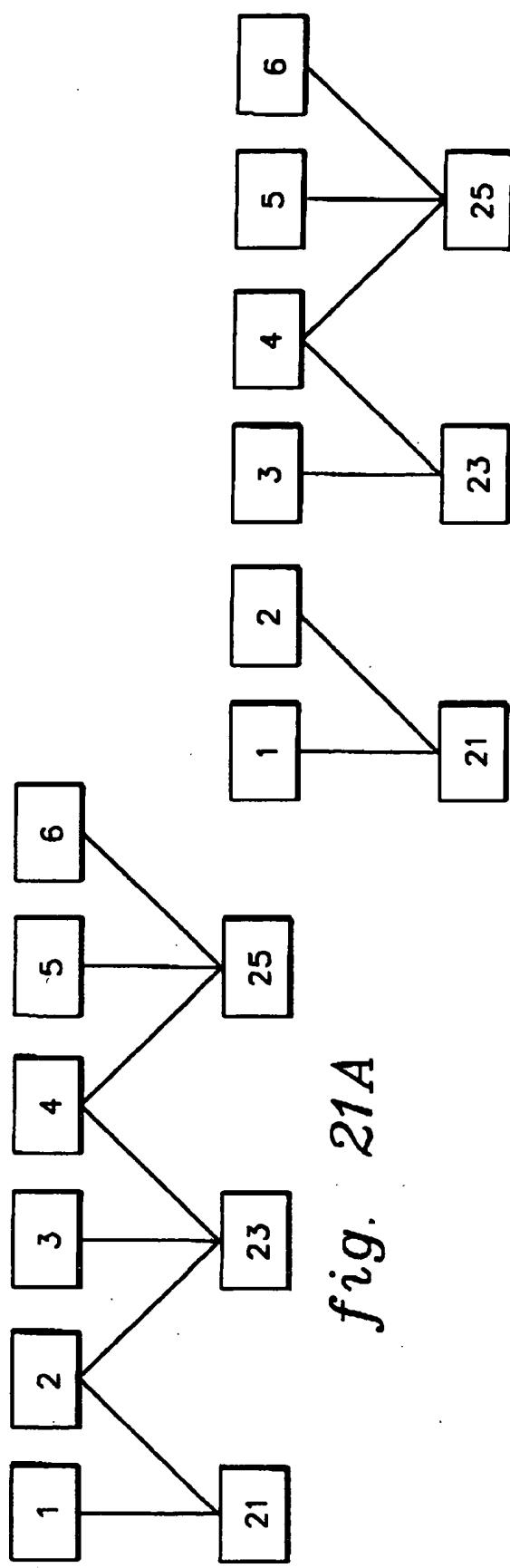


fig. 21A

fig. 21B

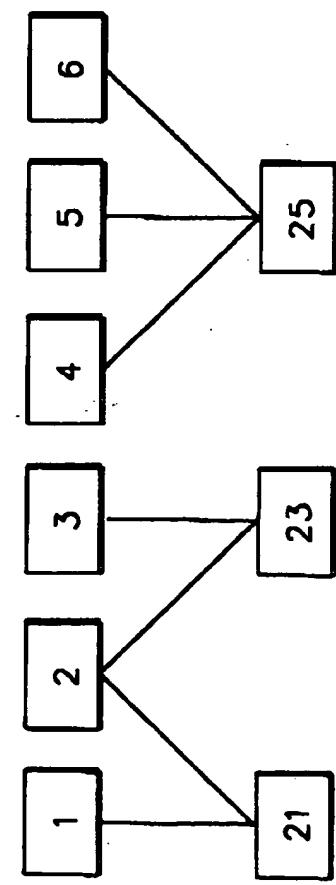


fig. 21C

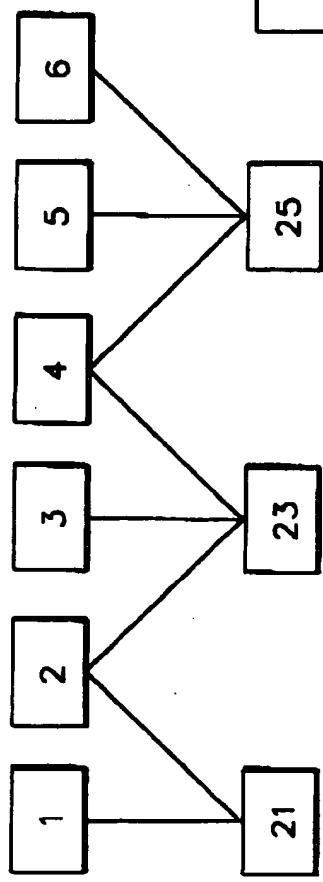


fig. 22A

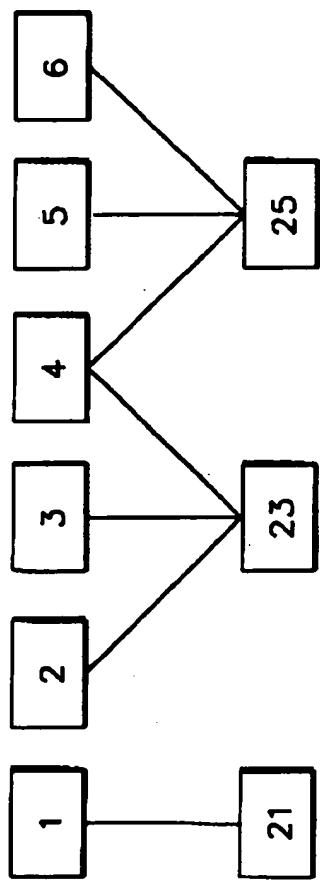


fig. 22B

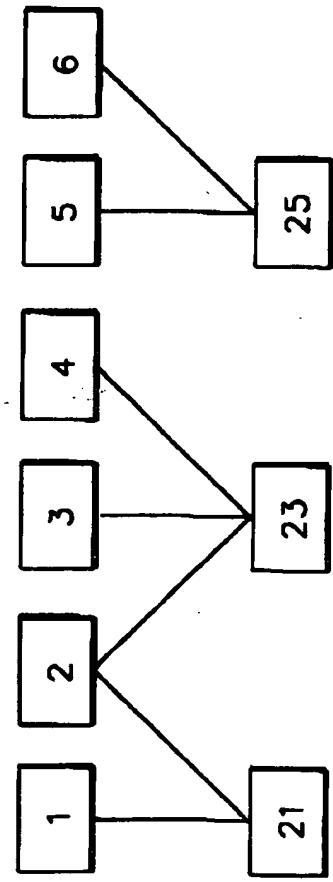


fig. 22C